Fig.

~36a MONITOR 42 38a COMPRESSION/ EXPANSION 38 RECORD/ REPRODUCTION 38a 8 40a ~ 36a SIGNAL PROCESSOR SYSTEM CONTROLLER 36 ~18a 3 34 34a 9 ~20a ADC 32a SIGNAL GENERATOR ~22e PRE-PROCESSING 1<u>8</u> 20 32 20a TIMING SIGNAL GENERATOR $\sim 22c$ 31 IMAGE PICKUP ဓ္ဌ **4**a 22b 28 26 OPERATION PANEL DRIVER ,0

Fig. 2A

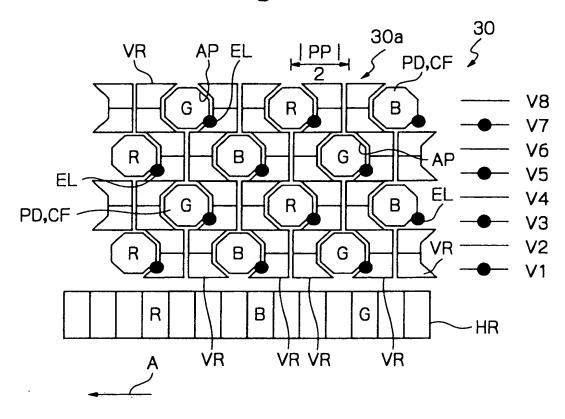
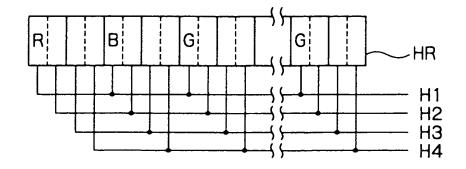


Fig. 2B



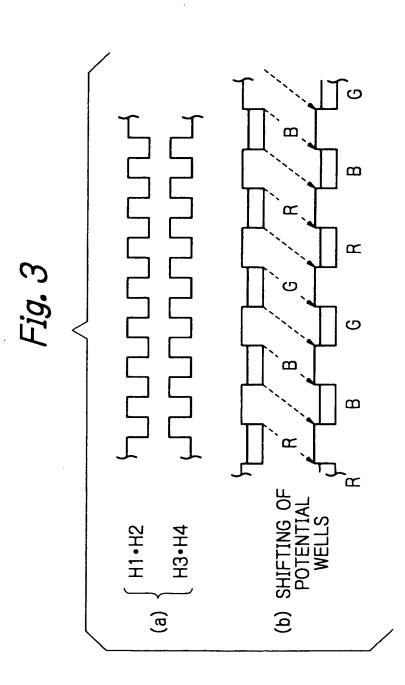


Fig. 4A

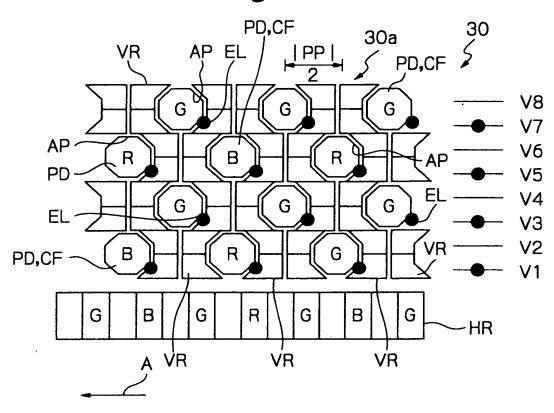


Fig. 4B

